



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 7801

SERIAL NUMBER 10/805,006	FILING OR 371(c) DATE 03/18/2004 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 2102651-991140	
APPLICANTS Mutsunori Igarashi, Kanagawa-ken, JAPAN; Fumihiro Minami, Kanagawa-ken, JAPAN; <i>NONE NL</i> ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** <i>VERIFIED NL</i> JAPAN P2003-081321 03/24/2003 IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/03/2004					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and <i>Allowance</i> Acknowledged <i>Nelson Lam</i> <i>NL</i> Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 18 4	INDEPENDENT CLAIMS 10 4
ADDRESS 26379					
TITLE Integrated circuit device, clock layout system, clock layout method, and clock layout program					
FILING FEE RECEIVED 1502	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		